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- NOTE

An Arabian Troubleshooting Contacts included as part of this Schematic Package Supplement. The Arabian Troubleshooting Guide contains Atari CAT Box' troubleshooting procedures and signal name descriptors.



Schematic Package Supplement to

encove this staple to separa e schematic diagrams fro e Troubleshooting Guide



Includes
Troubleshooting Guide

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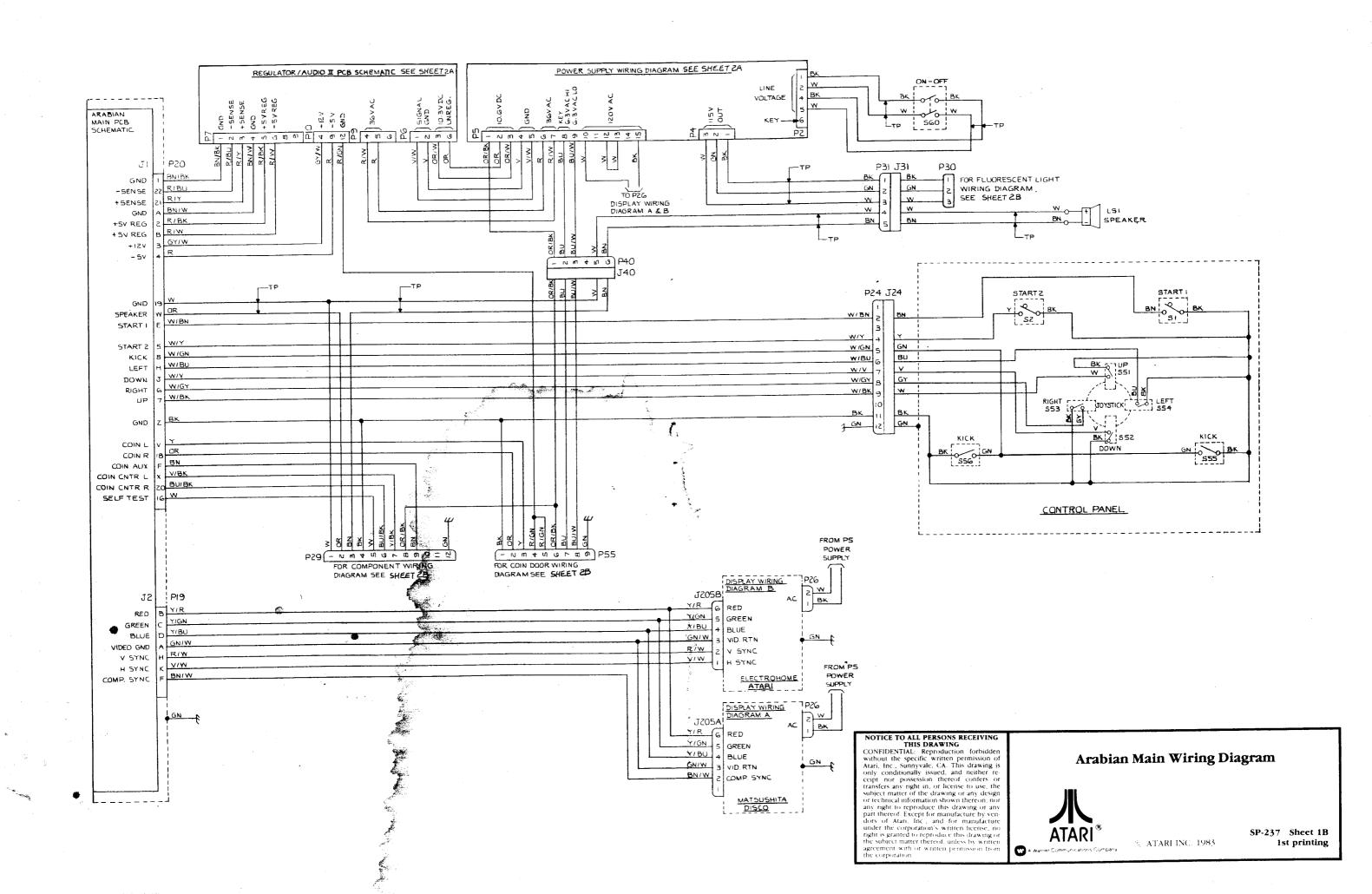
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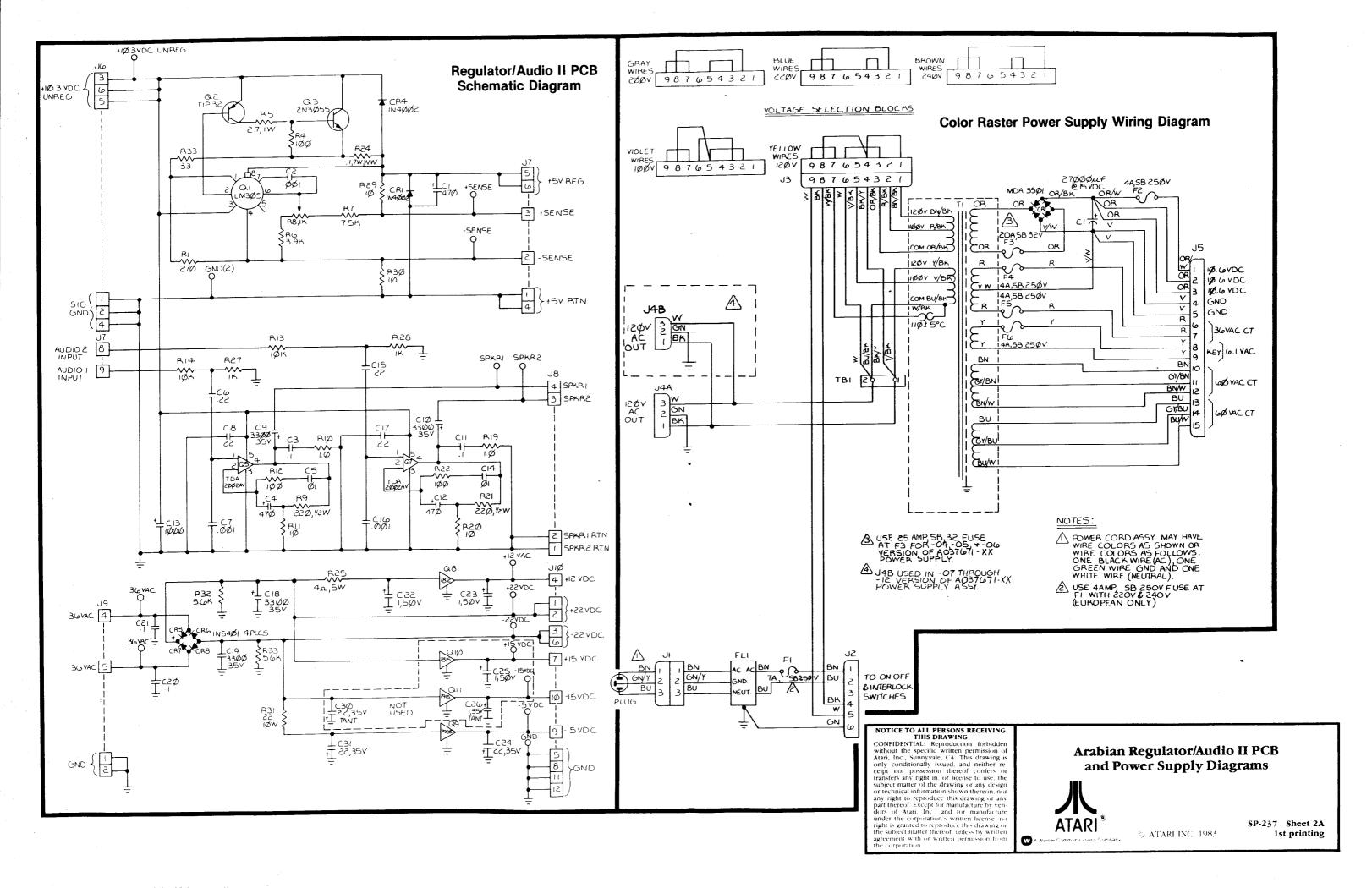
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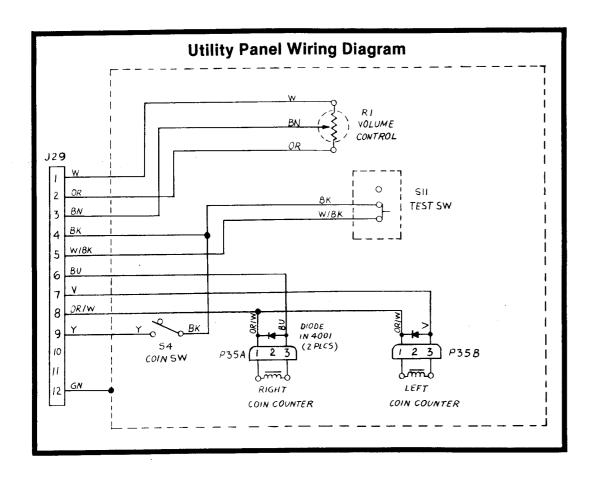


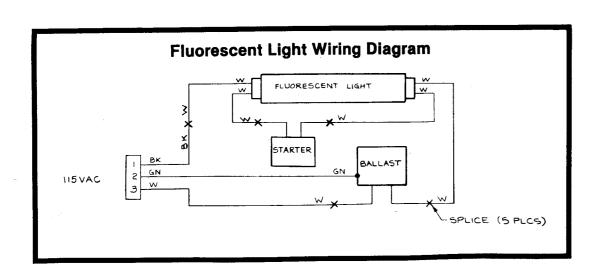
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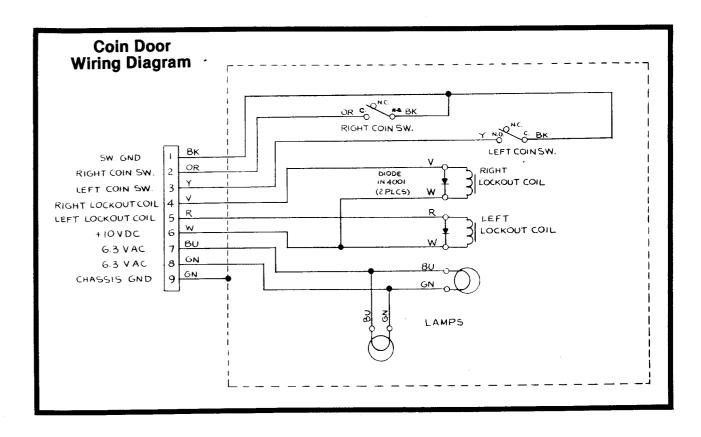












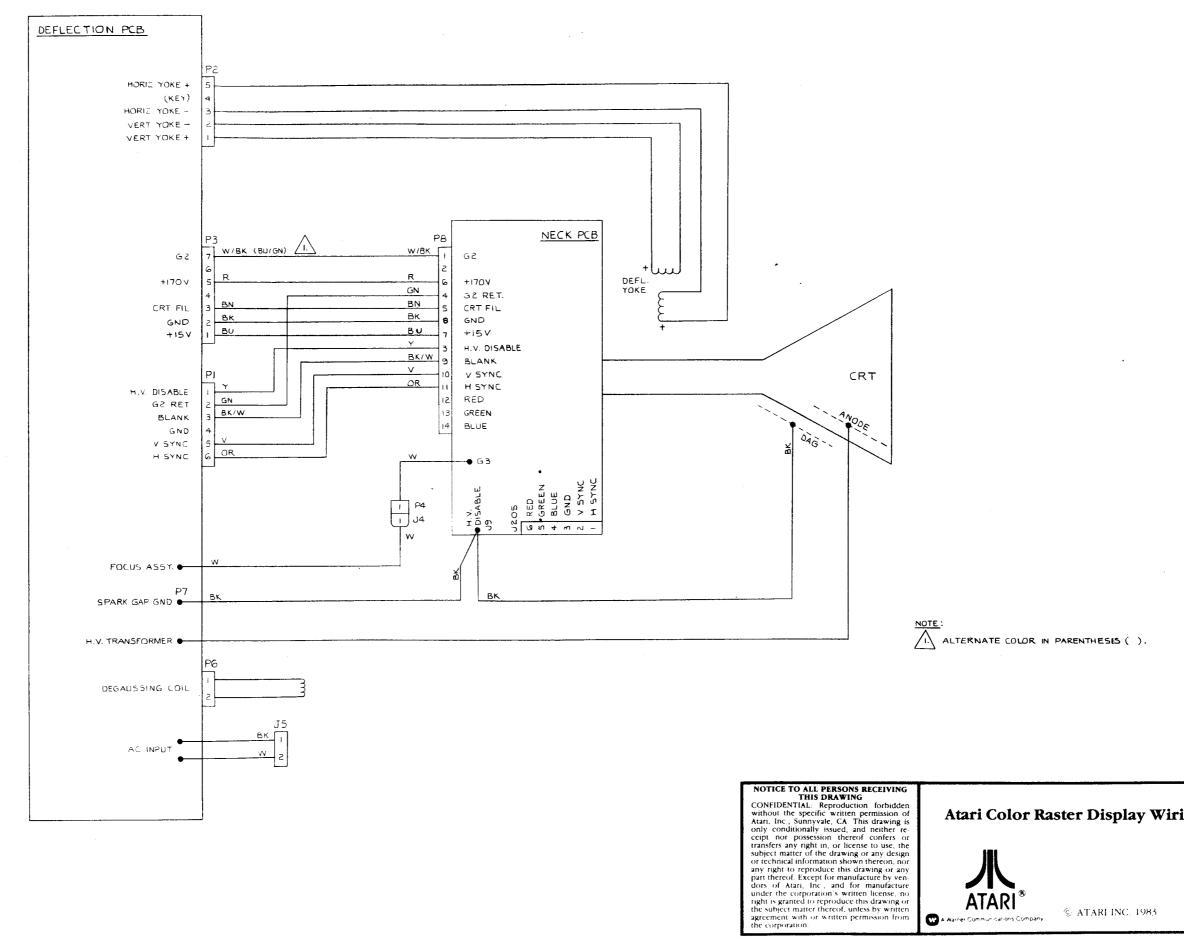
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Arabian Game Interfaces



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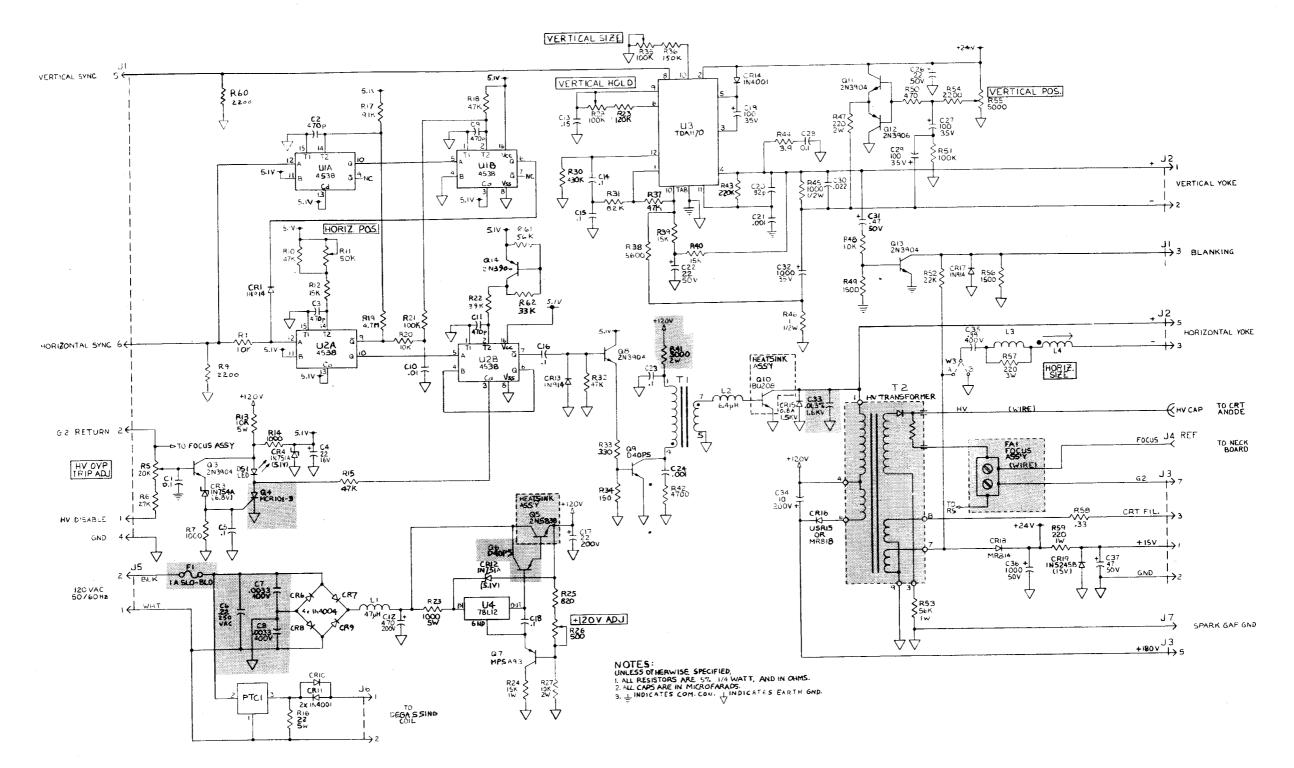


Atari Color Raster Display Wiring Diagram



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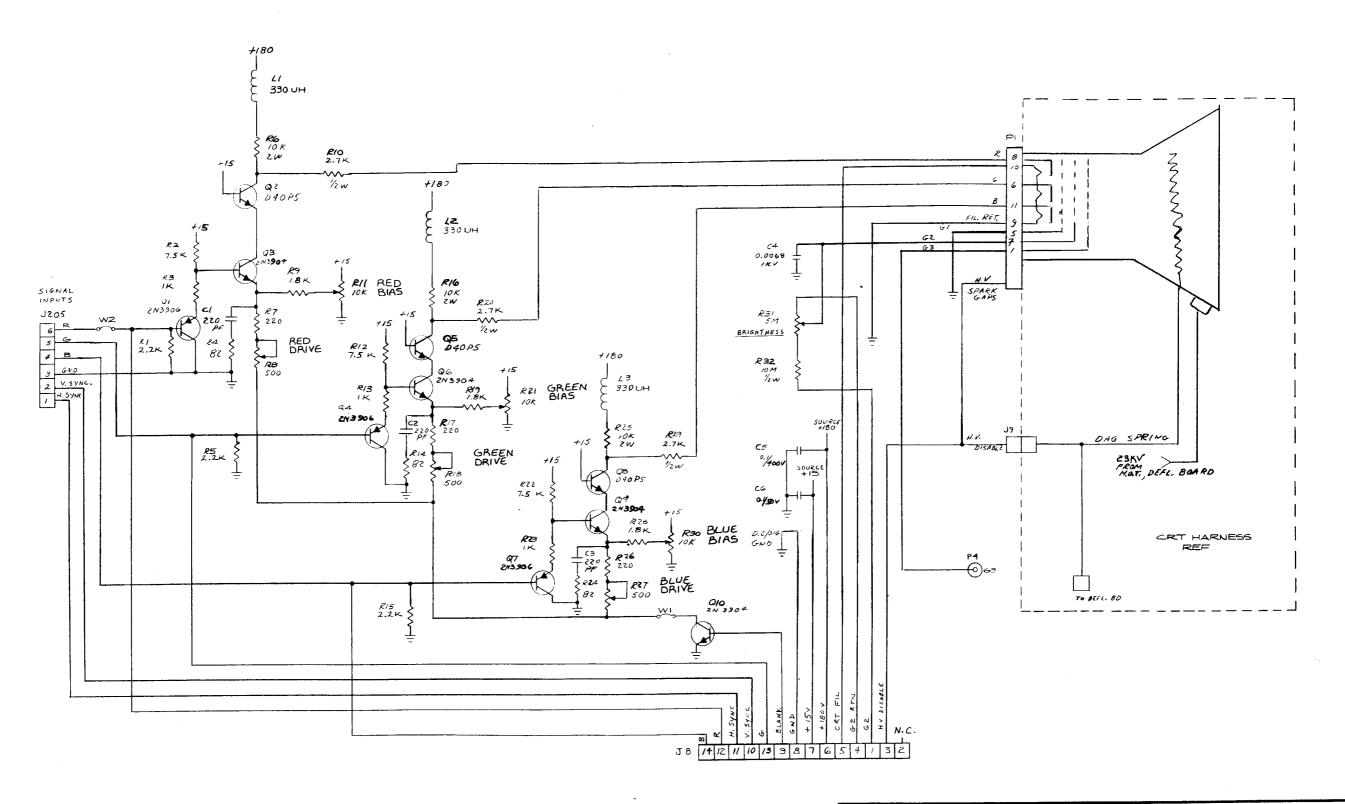
The shaded areas of this schematic indicate components whose values are of special significance to product safety. Should any component in the shaded areas need to be replaced, use only the value given in the parts lists. Do not deviate from the resistance, wattage, and voltage values shown.

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Atari Color Raster Display Deflection PCB Schematic Diagram



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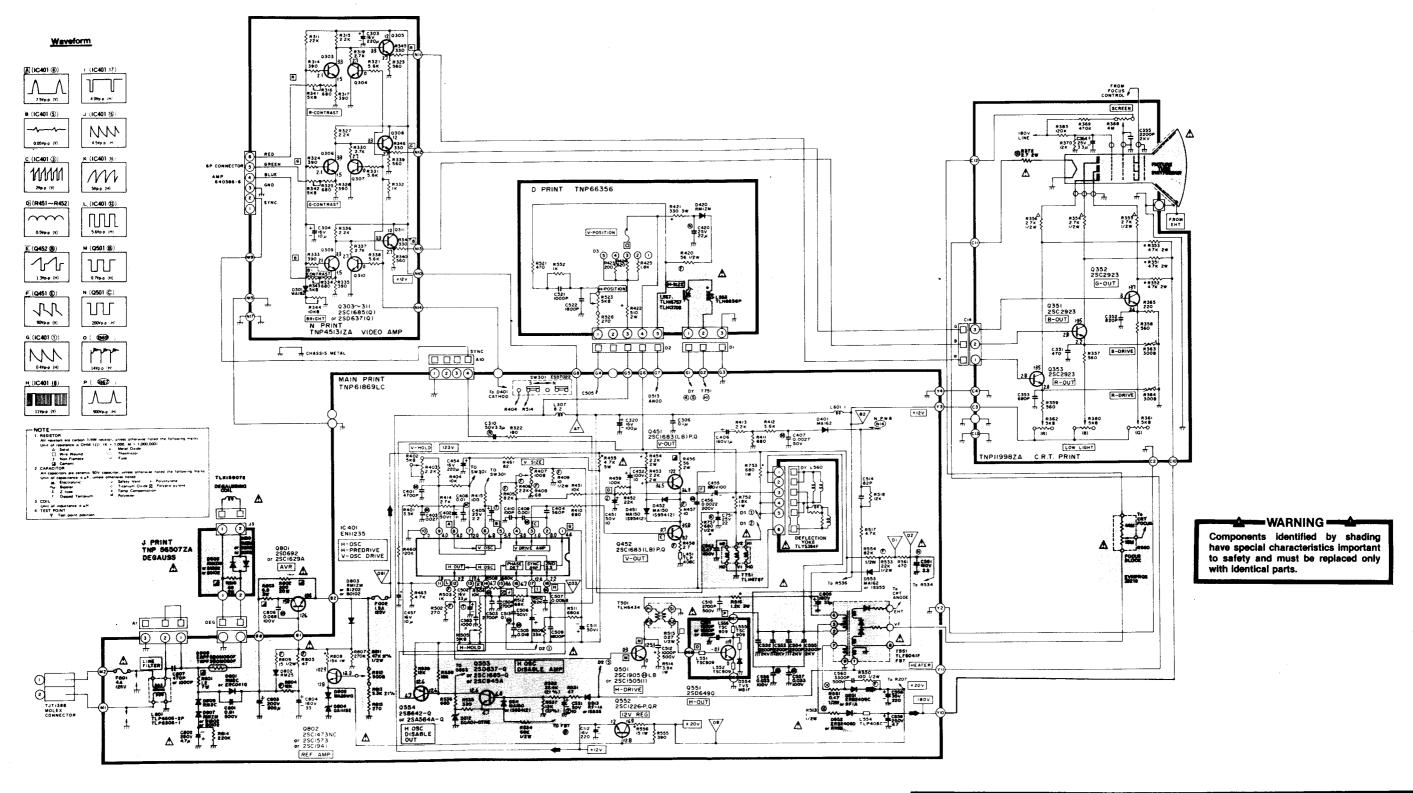


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Atari Color Raster Display Neck PCB Schematic Diagram



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Matsushita Color Raster **Display Schematic Diagram**



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Arabian Memory Map

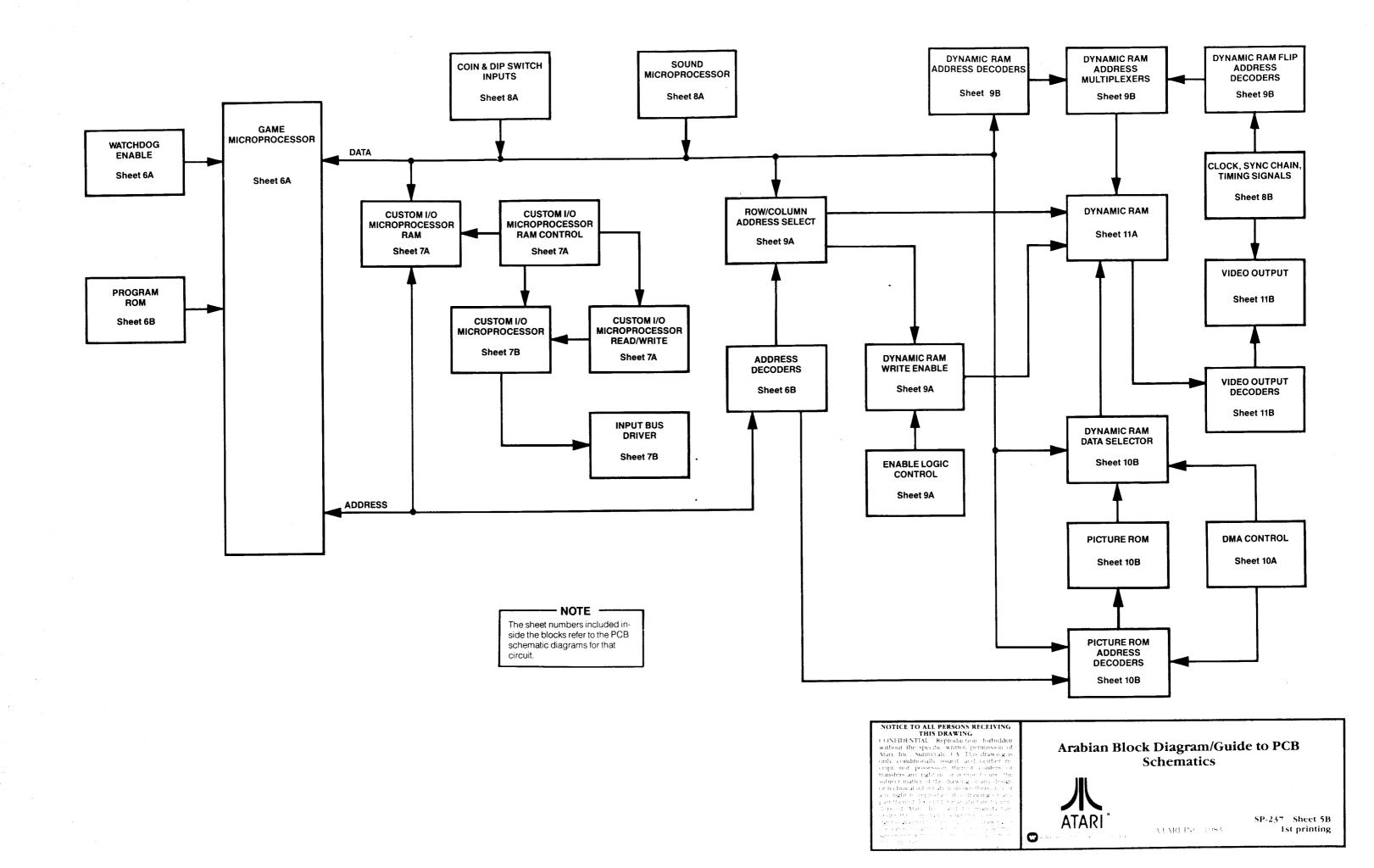
HEXA- DECIMAL ADDRESS	A15	A14	A13	A12	AI A11	DDR A10	ESS A9	BUS A8	SIG A7	NAI A6	LIN A5	ES A4	A3	A2	A1	AO	READ/ WRITE					IGNA D3				FUNCTION
E006	1	1	1	0	A	A	A	A	A	A	A	A	Α	1	1	0	W	D	D	D	D	D	D	D	D	\overline{XY} (H) Picture Size and
E005	1	1	1	0	Α	Α	Α	Α	Α	Α	Α	Α	Α	1	0	1	W	D	D	D	D	D	D	D	D	XY (L) DMA Start
E004	1	1	1	0	Α	Α	Α	Α	Α	Α	Α	Α	Α	1	0	0	W	D	D	D	D	D	D	D	D	WR (H) DMA RAM
E003	1	1	1	0	Α	A	A	A	A	Α	Α	Α	Α	0	1	1	W	D	D	D	D	D	D	D	D	$\overline{WR}(\overline{L})$ Start Address
E002	1	1	1	0	Α	Α	A	Α	A	Α	Α	Α	Α	0	1	0	W	D	D	D	D	D	D	D	D	$\overline{\mathrm{RD}}(\overline{\mathrm{H}})$ DMA ROM
E002 E001	1	1	1	0	A	A	A	A	A	A	A	A	A	0	0	1	W	D	D	D	D	D	D	D.	D	$\overline{RD}(\overline{L}) \int Start Address$
E000	1	1	1	. 0	A	A	A	A	A	A	A	A	A	0	0	0	W					D	D	D	D	BSEL Bank Select
DFFF-D000	1	1	0	1	A	A	A	A	A	Α	Α	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	Custom Microprocessor RAM
CA00	1	1	0	0	1	0	1	0	A	Α	Α	Α	Α	Α	Α	A	R/W	D	D	D	D	D	D	D	D	Sound Chip Data
C800	1	1	0	0	1	0	0	0	A	A	A	A	Α	Α	A	Α	W	D	D	D	D	D	D	D	D	Sound Chip Address
C200	1	1	0	0	0	0	1	0	Α	Α	Α	Α	Α	Α	Α	Α	R					D	D	D	D	Option Switches
C000	1	1	0	0	0	0	0	0	A	Α	Α	A	A	A	A	A	R					D	D	D	D	Coin Inputs
BFFF-8000	1	0	1	1	A	A	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D	D	D	Bit Map RAM
6000-7FFF	0	1	1	A	A	A	A	Α	A	Α	Α	Α	Α	Α	Α	Α	R	D	D	D	D	D	D	D	D	ROM 3
4000-5FFF	0	1	0	A	A	A	A	A	A	A	A	Α	Α	Α	A	Α.	R	D	D	D	D	D	D	D	D	ROM 2 -Program ROM (Z-80)
2000–3FFF	0	0	1	A	A	A	A	A	A	Α	Α	Α	Α	Α	Α	Α	R	D	D	D	D	D	D	D	D	ROM I
0000-1FFF	0	0	0	A	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D	D	D	$\overline{ROM}\overline{0}$

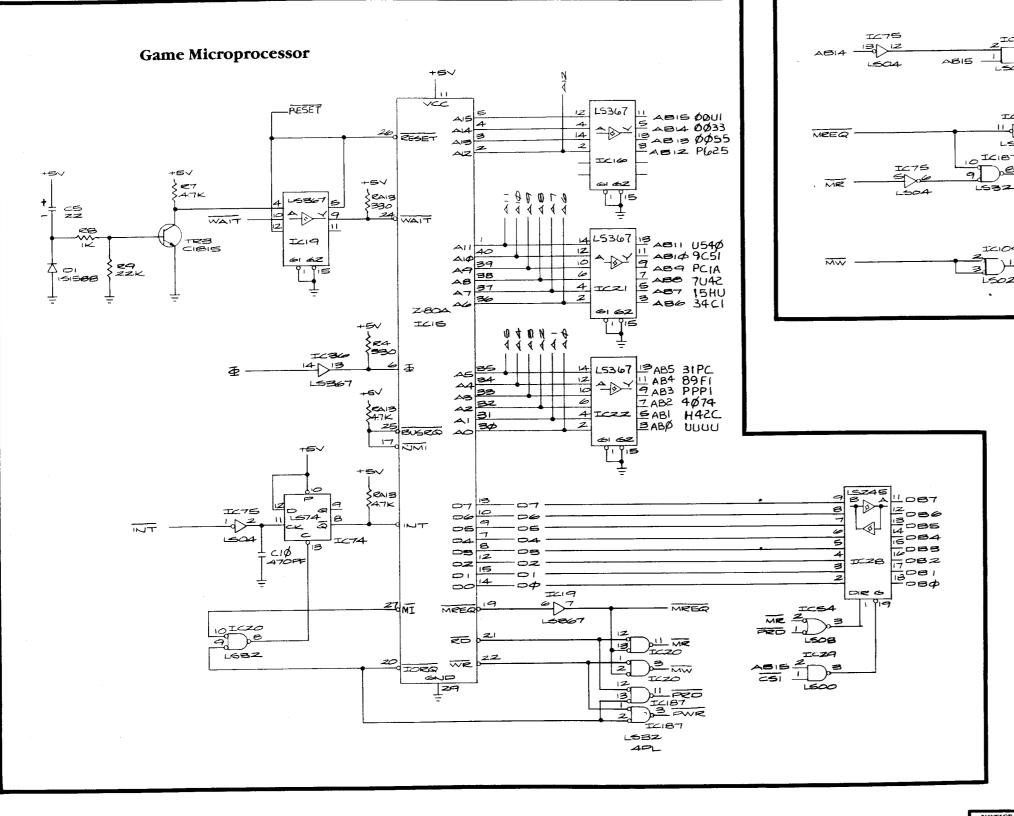
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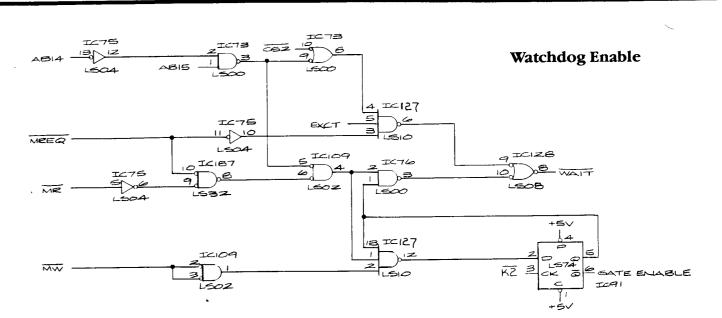
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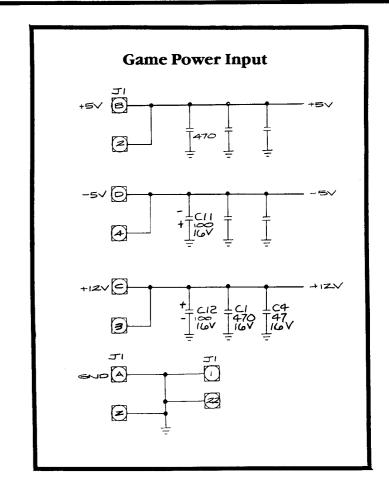


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NOTES:

DENDTES JI CONNECTOR

DENOTES JZ CONNECTOR

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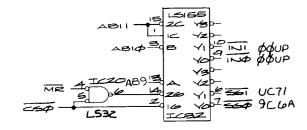
Arabian PCB Schematic Diagram

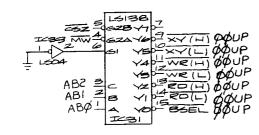


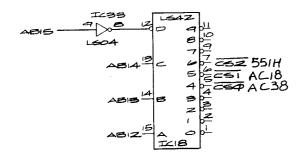
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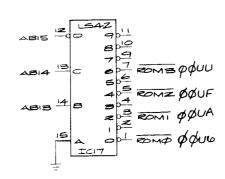
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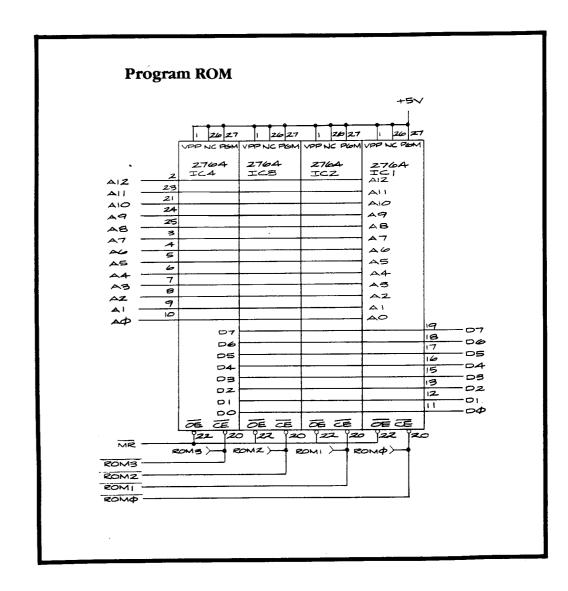
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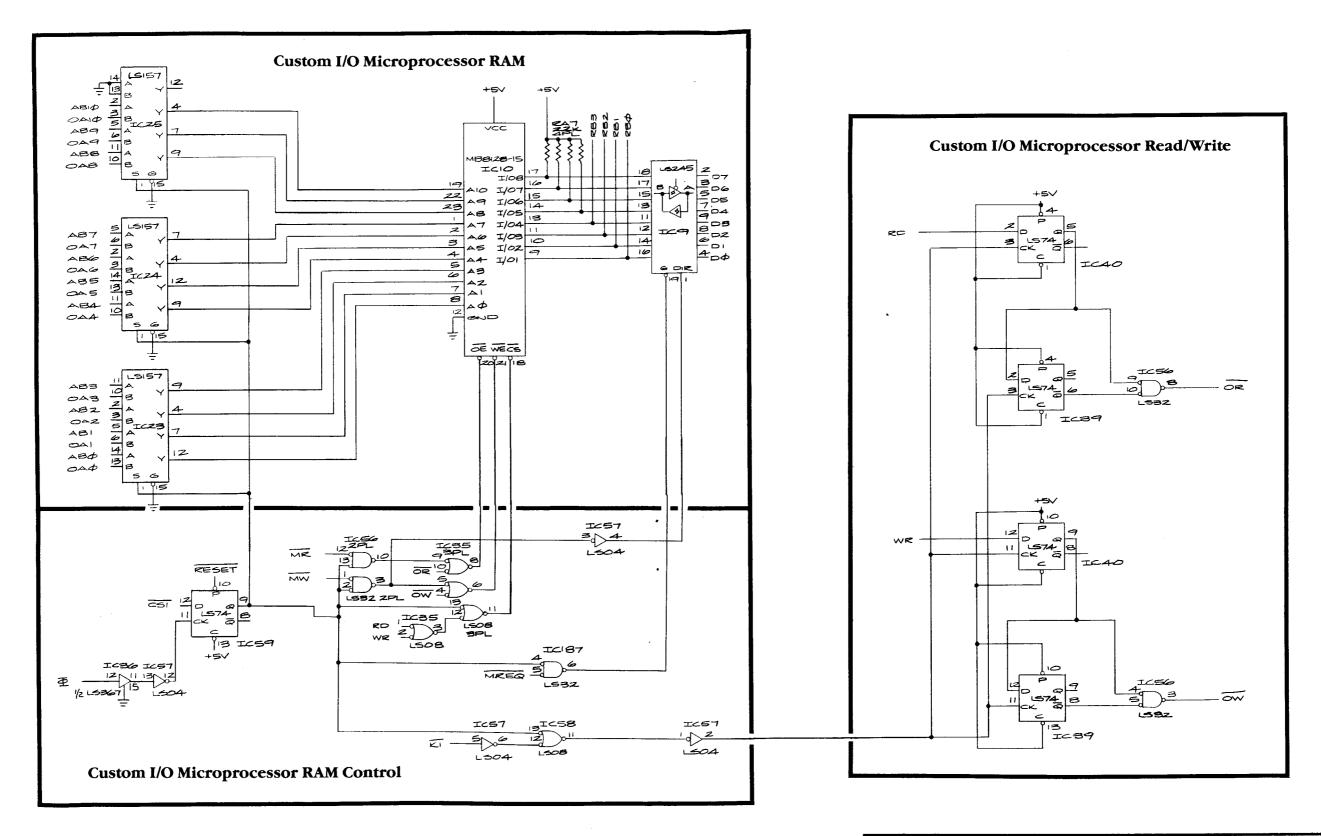
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Arabian PCB Schematic Diagram



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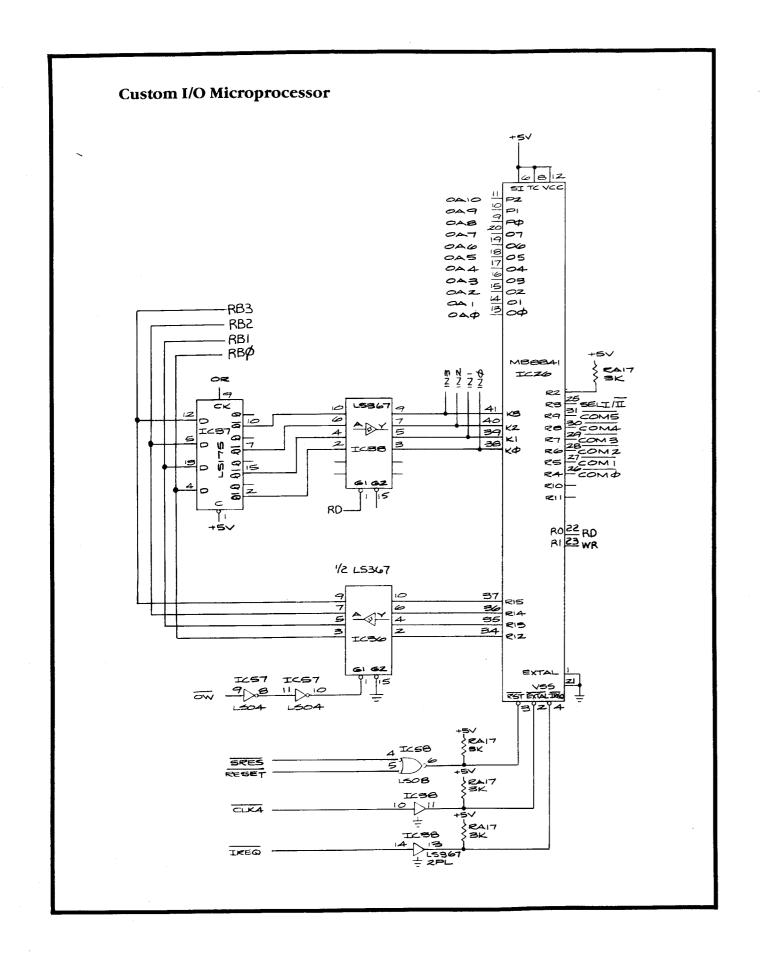


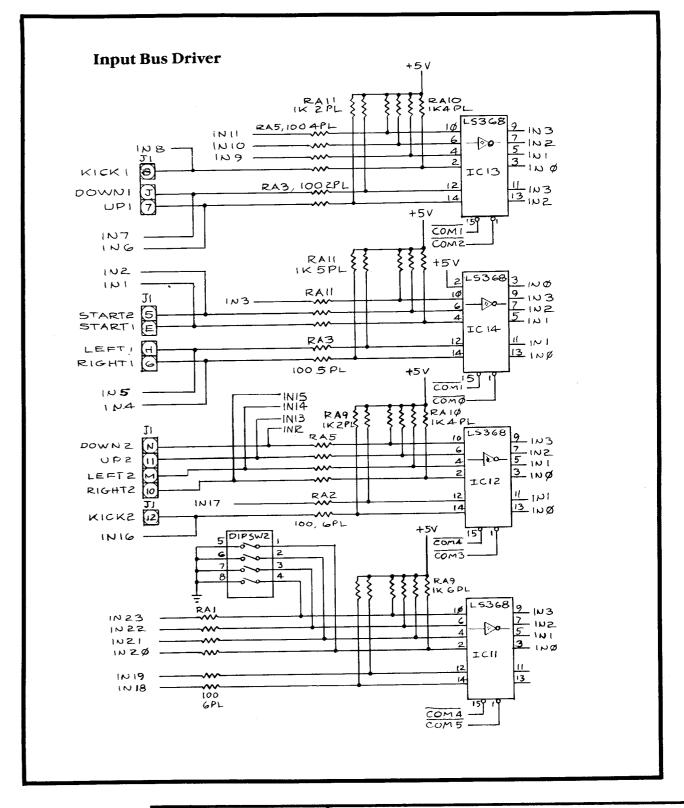
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Arabian PCB Schematic Diagram



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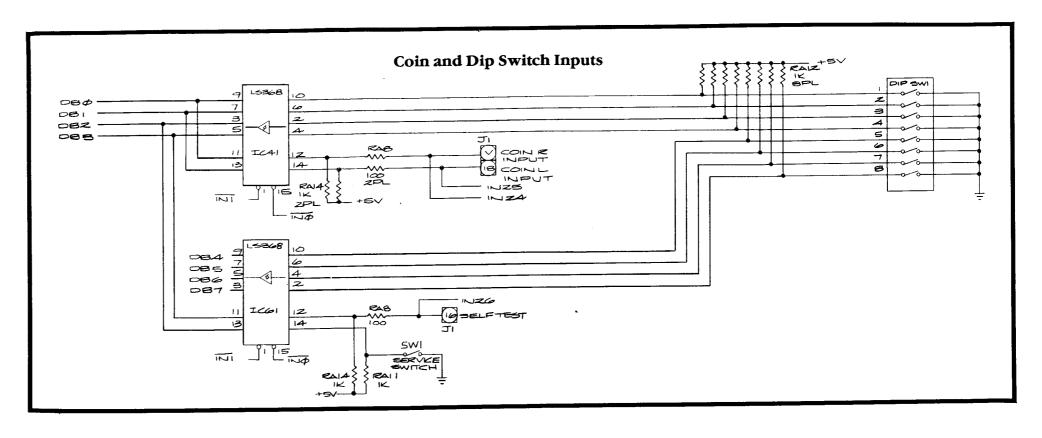
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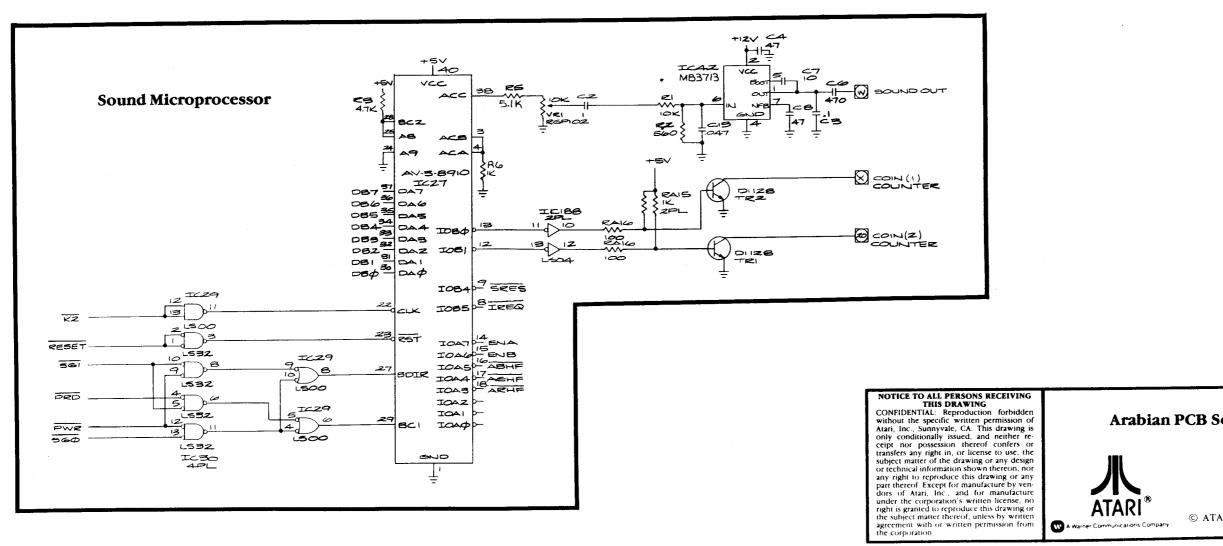
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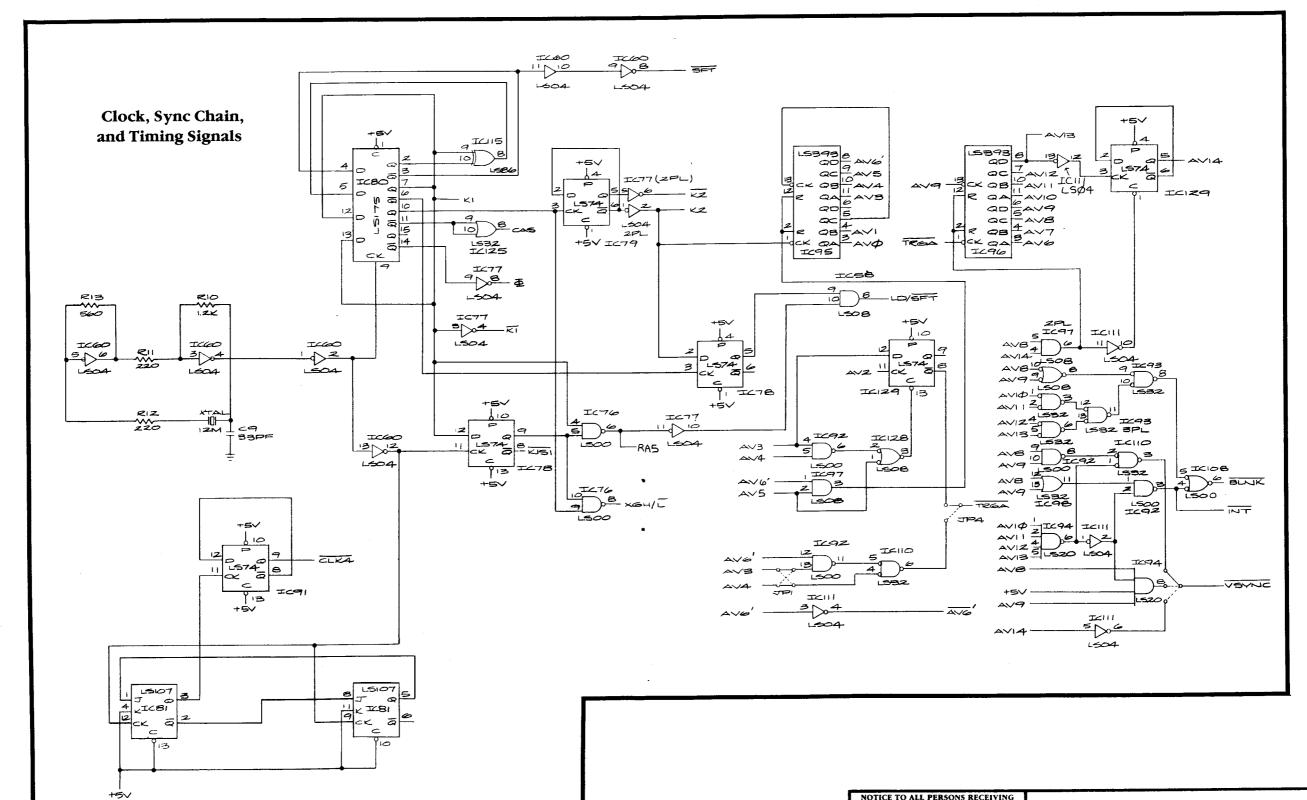


Arabian PCB Schematic Diagram



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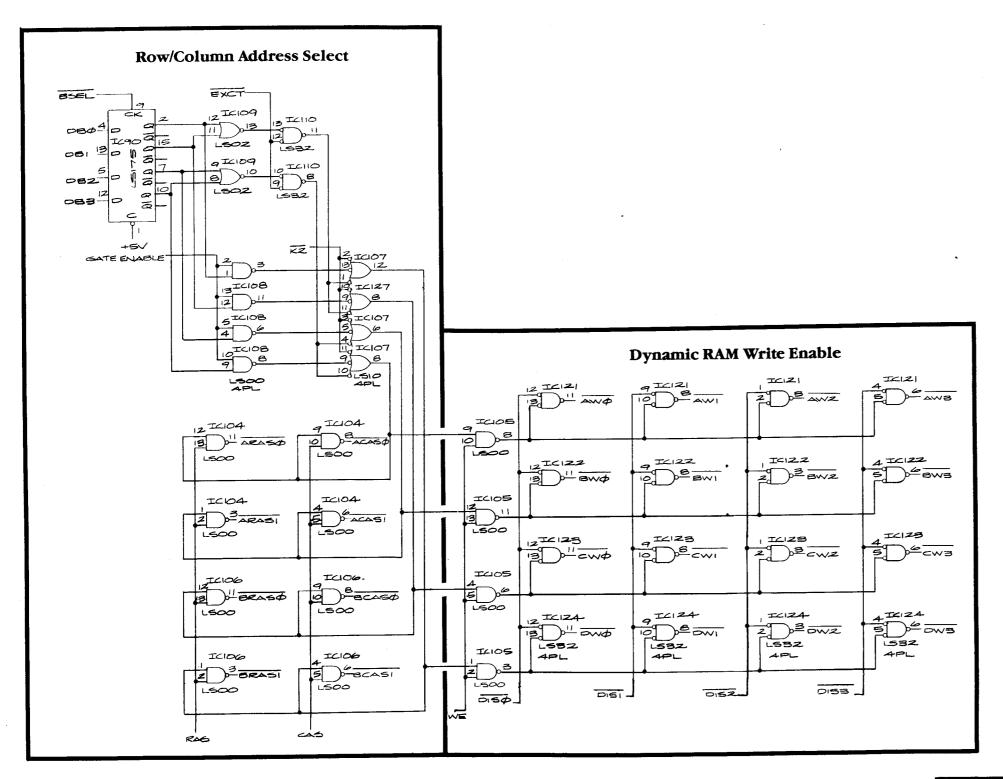


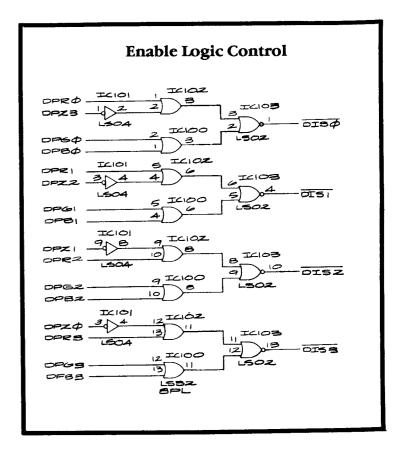
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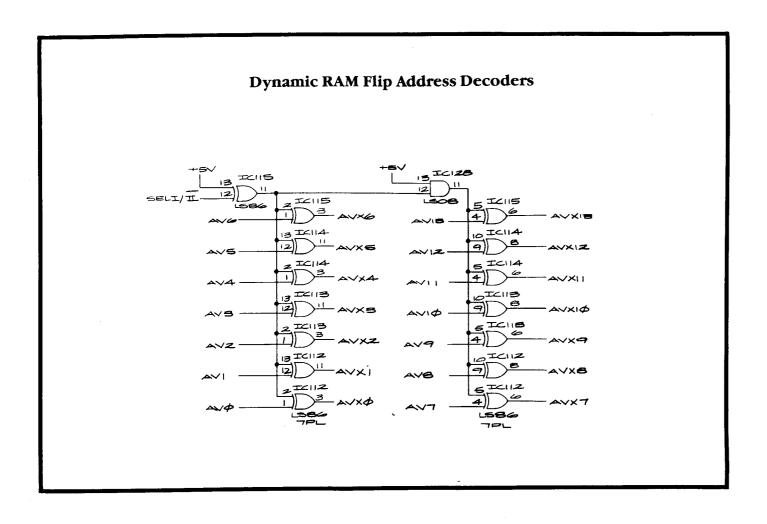


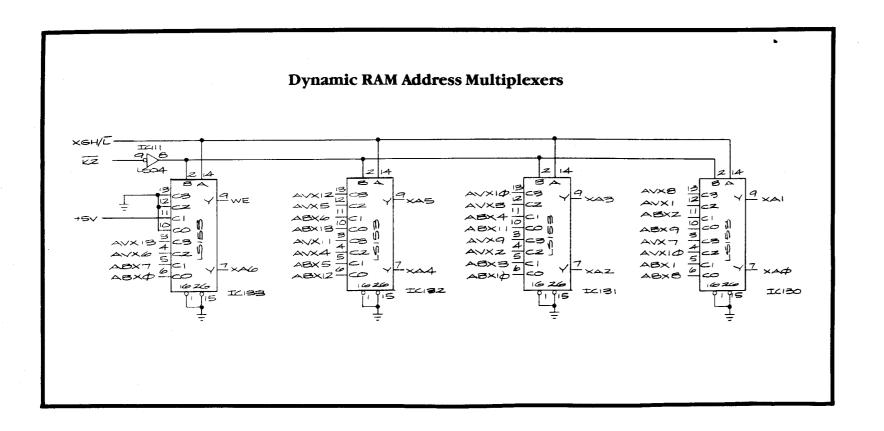
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Arabian PCB Schematic Diagram



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Dynamic RAM Address Decoders WR(L) A56 AB5 LO LO CK EPETC AB4 TC70 하 +5 IC71 EXCT ABI (C) 하 ABAS 4 ABXA IC51 +5~ エムラス 064 LD CK KT_ A812 **TC75** I472 +54 EXCT ABII M ac ABIO DBI AB9 CK AB8 101 IC49 T453

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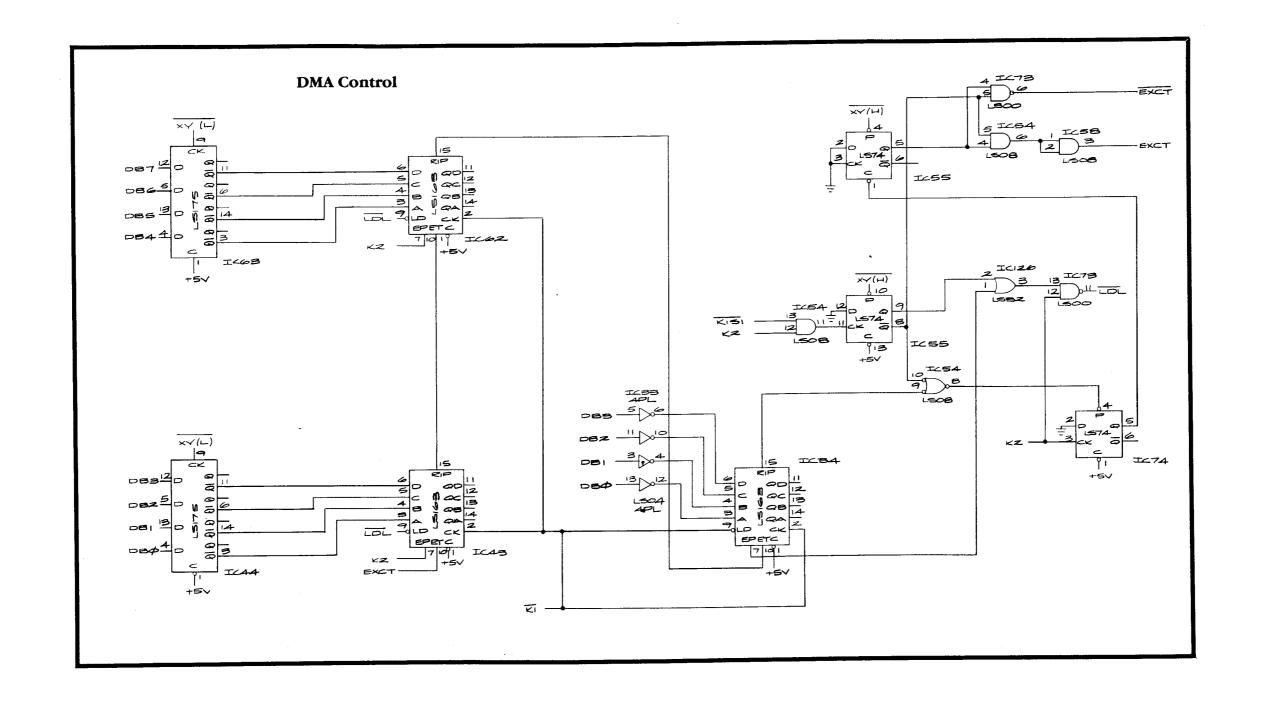
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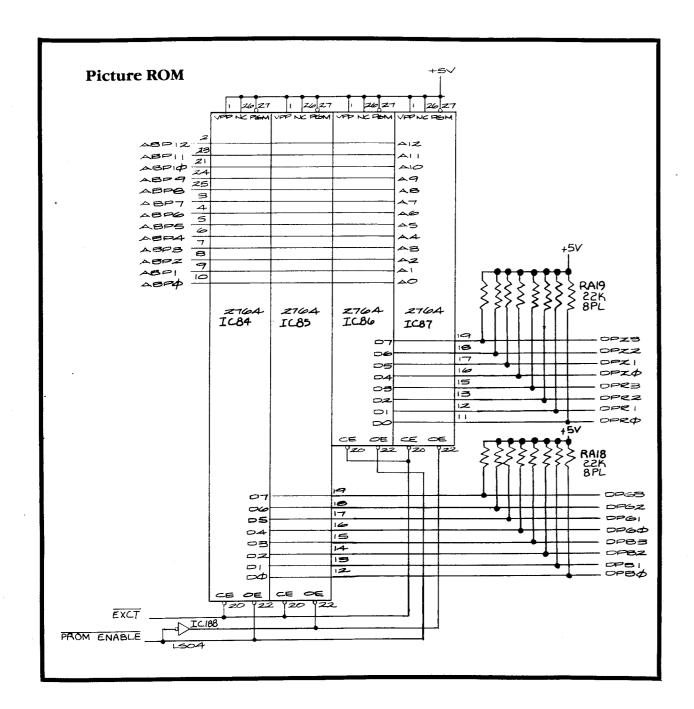


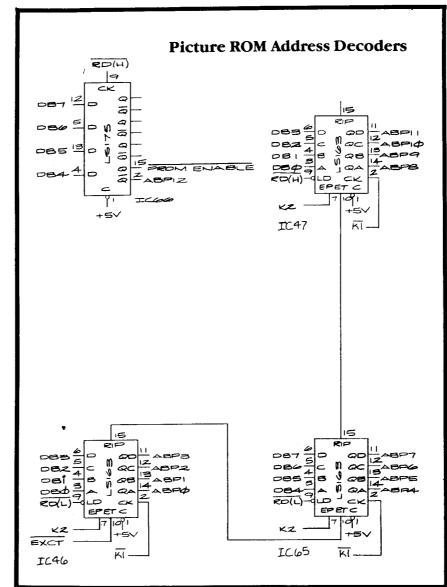
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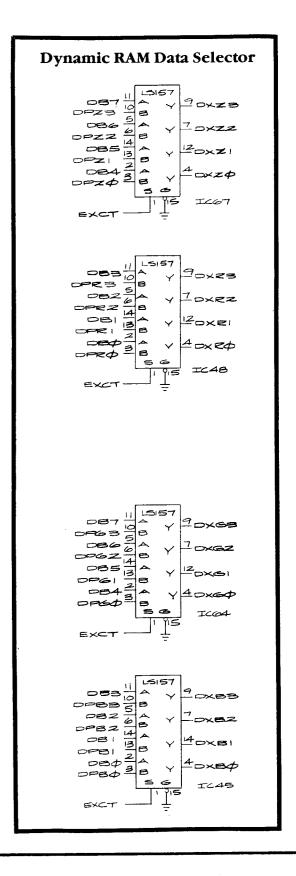
Arabian PCB Schematic Diagram



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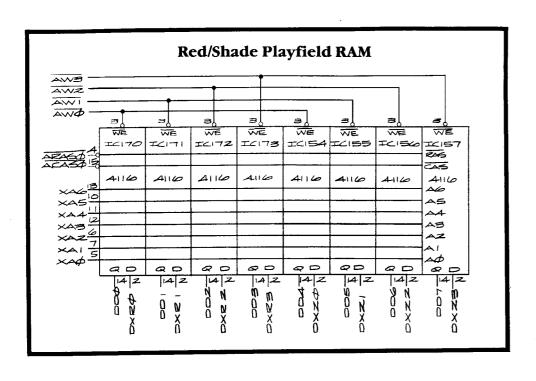
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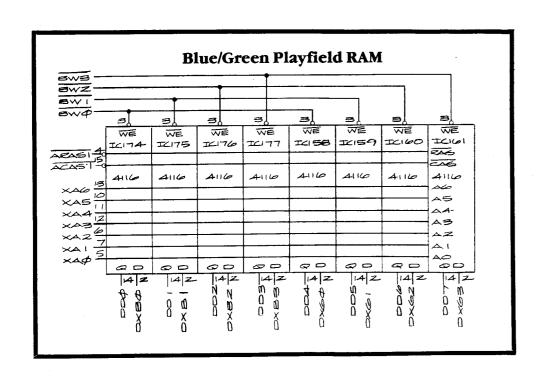
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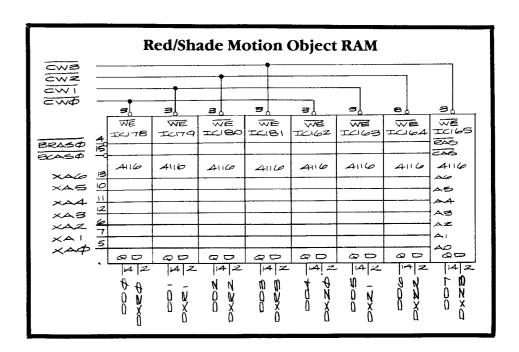
Arabian PCB Schematic Diagram

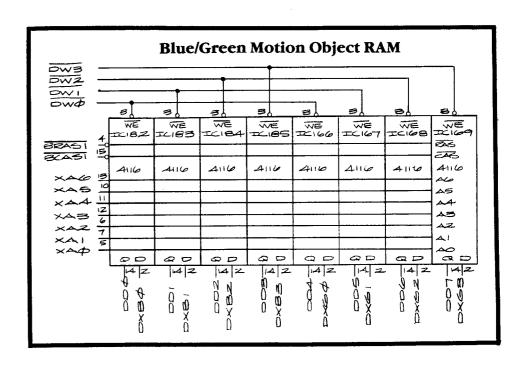


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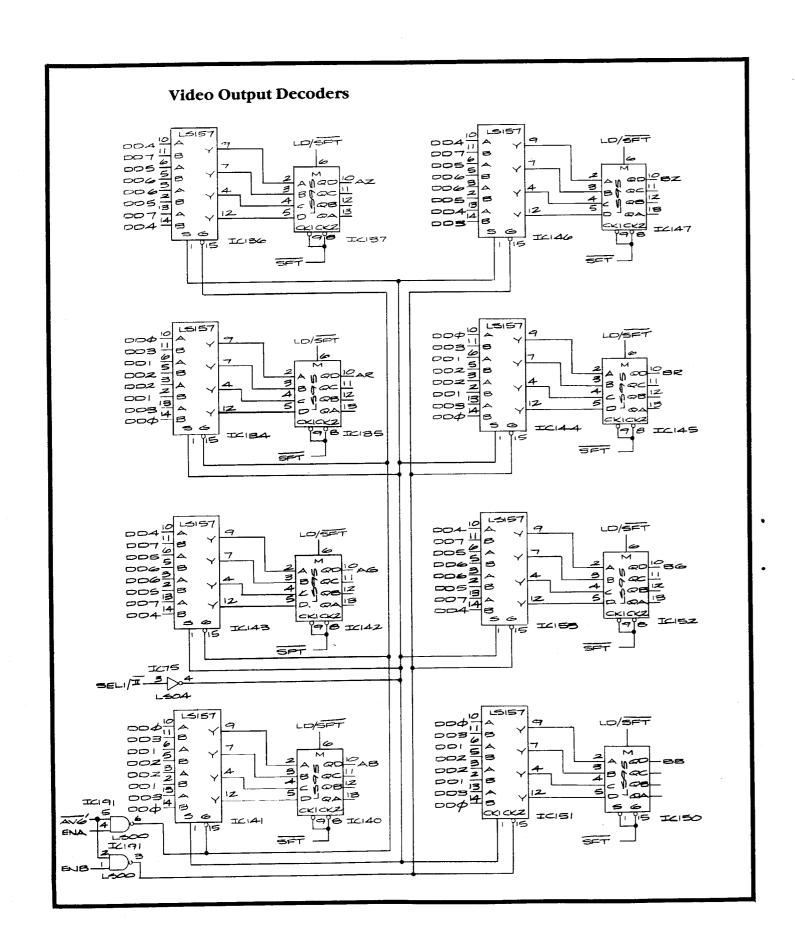
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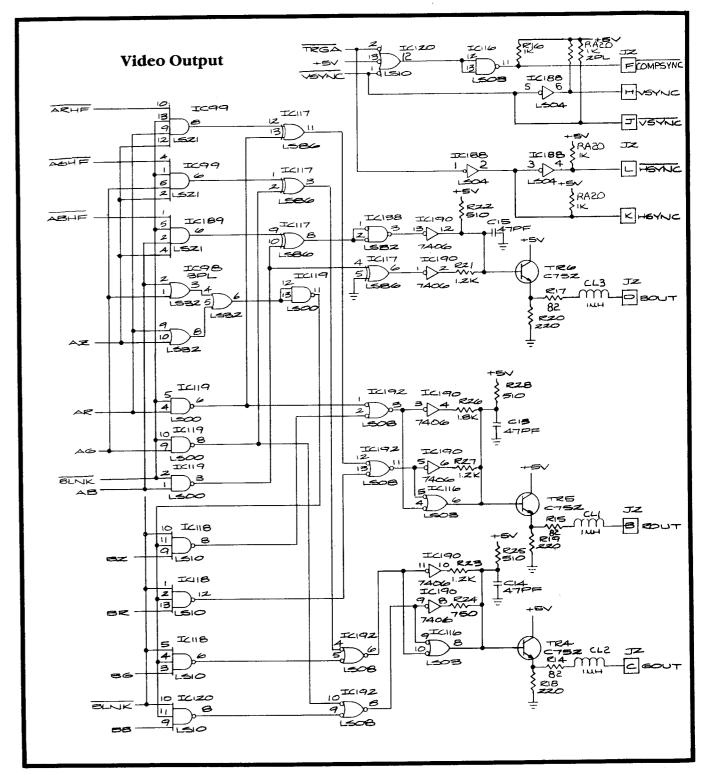
Arabian PCB Schematic Diagram



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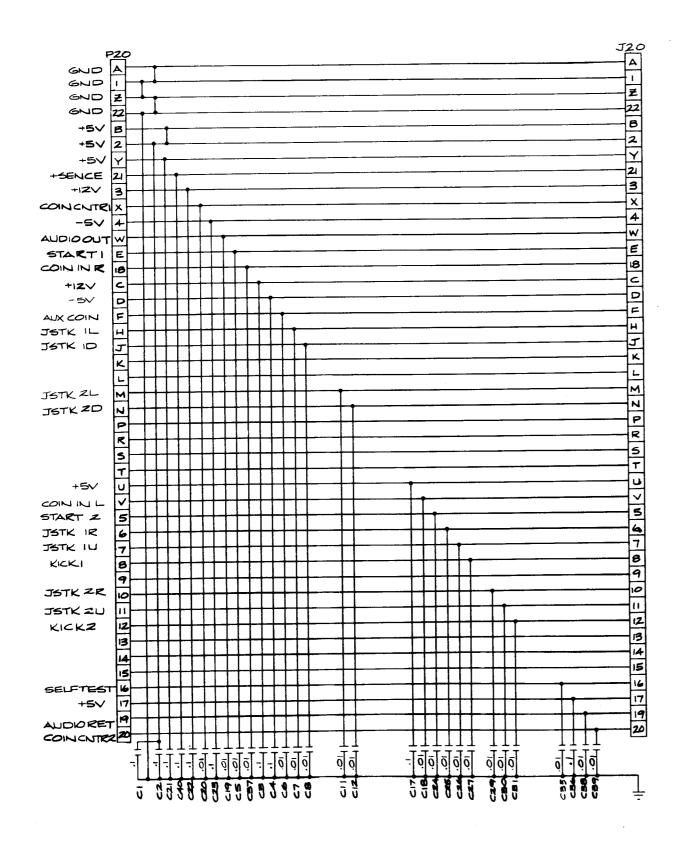


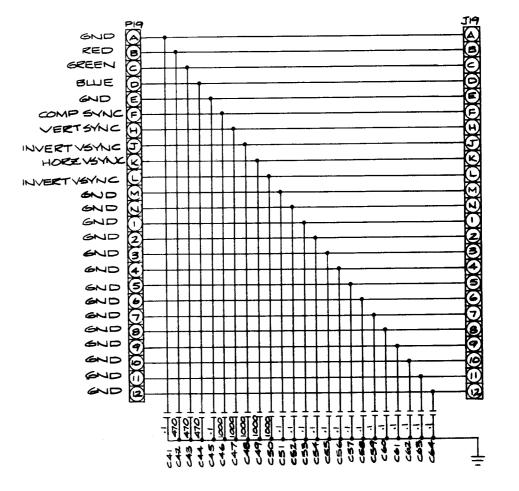
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Arabian PCB Schematic Diagram



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Arabian EMI Shield PCB Schematic Diagram



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